

**AMENDMENTS TO THE SPECIFICATION**

**Please add the following new paragraph after paragraph no. 0024<sup>22</sup>:**

<sup>22.1</sup>  
[0024.1] FIG. 11 is a sectional view of an illustrative embodiment of the present invention.

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**Please add the following new paragraph after paragraph no. 0057:**

[0057.1] FIG. 11 is a sectional view of an illustrative embodiment of the present invention. FIG. 11 illustrates a combination of FIG. 5(c) and FIG. 10, wherein the reference numerals 203a and 203b have been employed to more clearly reference the first and second channel regions, respectively. As shown in FIG. 11, a second gate electrode 107 is disposed above a first channel region 203a. Further, the low voltage gate electrode 110 is disposed above a second channel region 203b.